

FIG 1

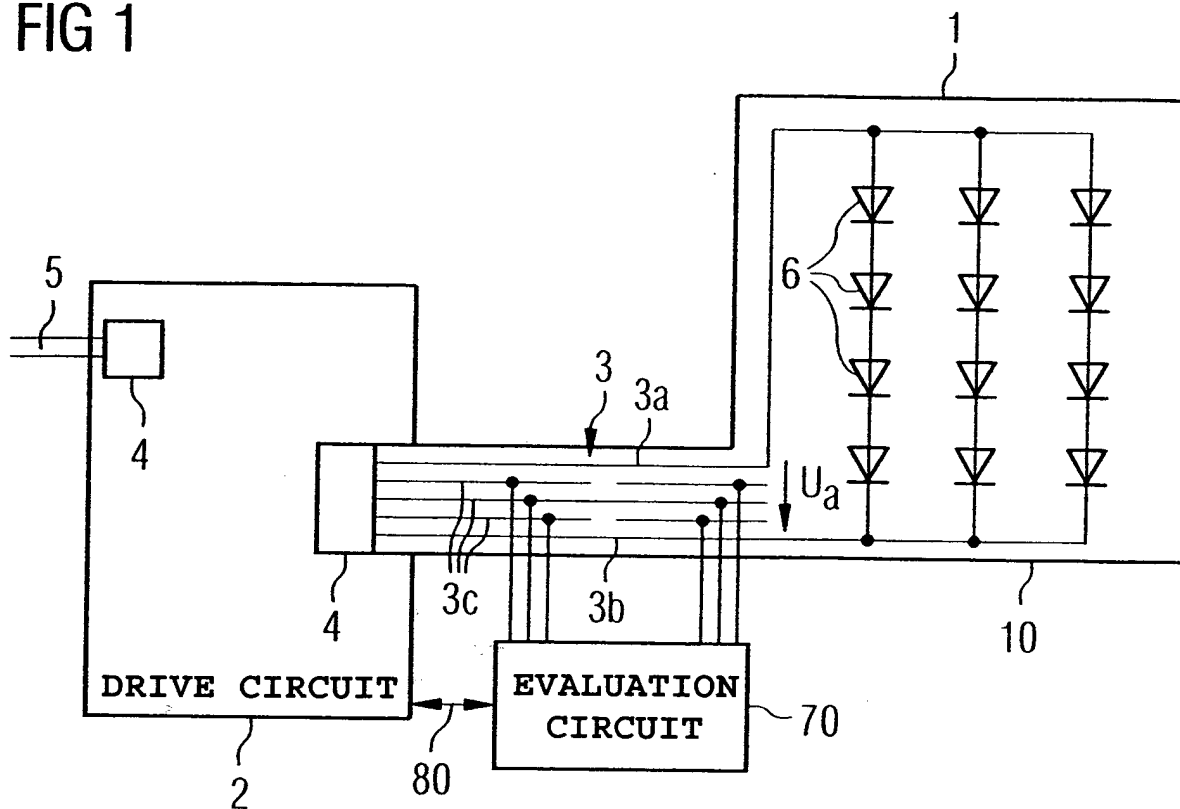


FIG 2

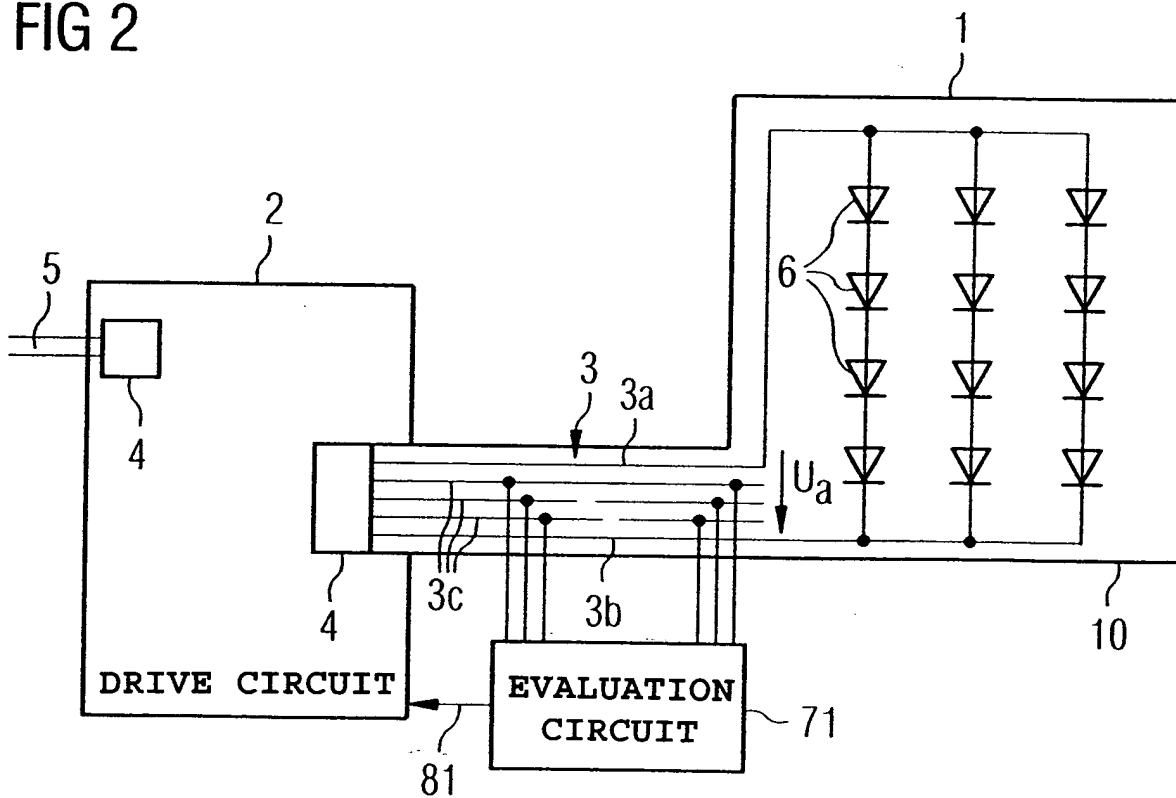


FIG 3

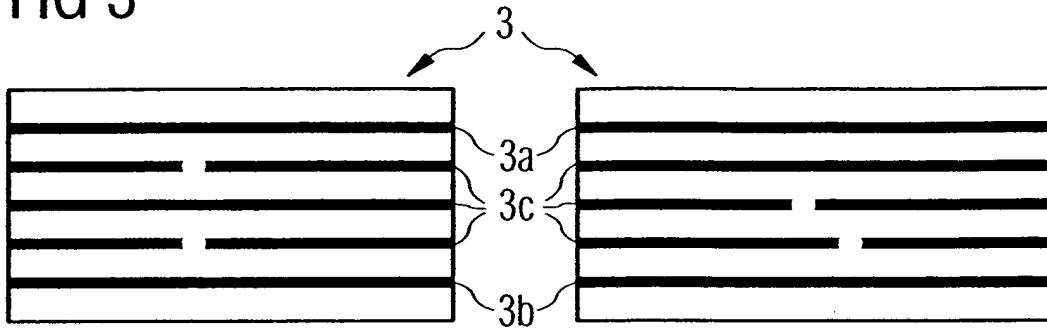


FIG 4

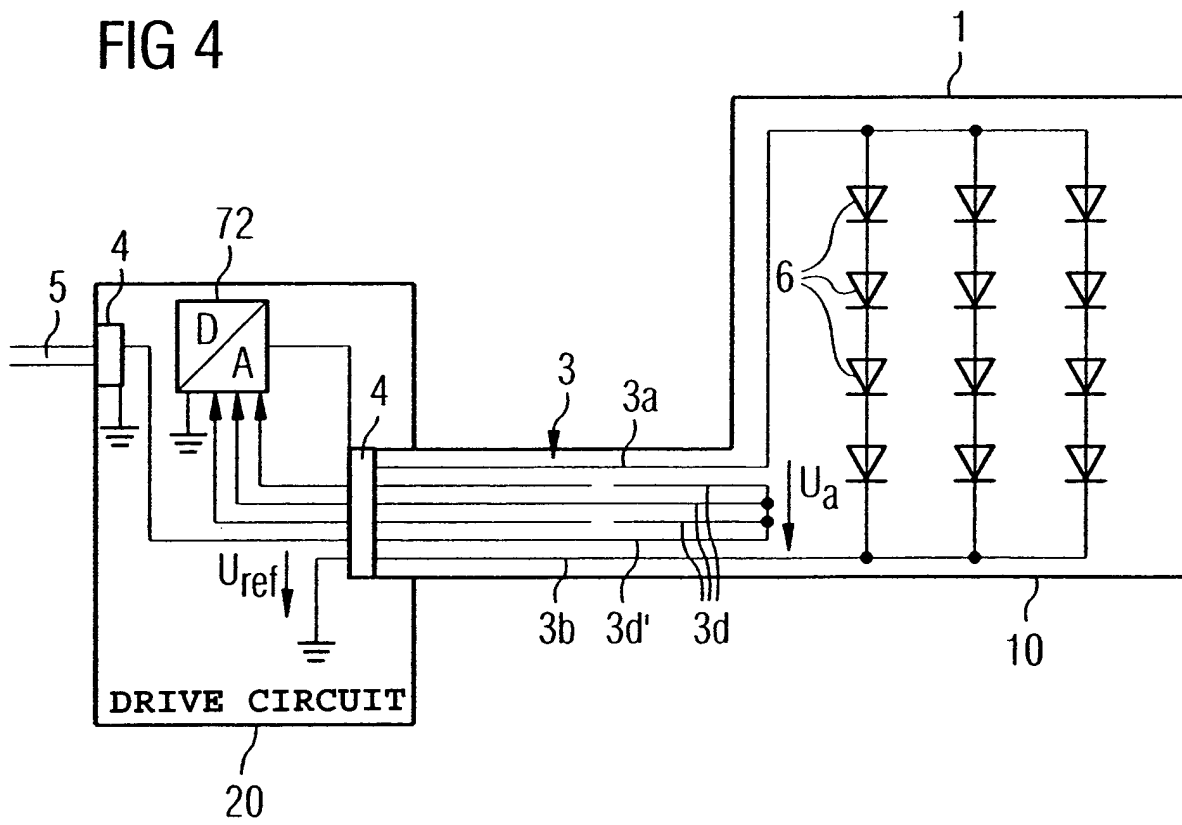


FIG 6

The diagram illustrates a drive circuit for a liquid crystal display. It features a **DRIVE CIRCUIT** (21) which includes a reference voltage source  $U_{ref}$  and a switch 5. The circuit is connected to a liquid crystal cell 3, which is modeled as a capacitor 4 and a resistor 3d. The cell is driven by a voltage  $U_a$ . The output of the cell is connected to a load resistor 10, which is a 3x3 grid of diodes. A diode array 6 is also connected to the circuit. The entire assembly is labeled 1.

[illegible]